



Mostly Online Project-Based Learning During the COVID-19 Pandemic

Matthew D'Souza^a, Arash Khatamianfar^b, and Chamith Wijenayake^a

School of Information Technology and Electrical Engineering, University of Queensland, Brisbane, Australia^a

School of Electrical Engineering and Telecommunications, University of New South Wales, Sydney, Australia^b

Corresponding Author Email: c.wijenayake@uq.edu.au

ABSTRACT

CONTEXT

Project based learning (PBL) has been used across many Engineering disciplines to successfully deliver courses to achieve better integration and blending of hands-on laboratory components with student learning; increased emphasis on applying the theoretical know how with practical problem solving; and integration of research with undergraduate teaching. During sudden transition to fully online teaching, courses having PBL components have been adversely affected. Some of the challenges included very limited or no access to laboratory resources, lack of opportunities for team-based projects and peer learning, limited interaction between students and teaching staff and poor student engagement. Therefore, successful delivery of PBL related courses in fully online or hybrid mode teaching requires a tailored approach. Experience sharing from successful examples of mostly online PBL teaching will therefore be beneficial for the broader educational community.

PURPOSE OR GOAL

In this paper, we share our experiences and lessons learnt in delivering Electrical Engineering courses having strong PBL components in two universities in Australia during the COVID-19 pandemic. To this end, we share the course details while outlining aims of using PBL in the courses, descriptions of PBL activities, methodologies and tools used to successfully deliver them during mostly online teaching.

APPROACH OR METHODOLOGY/METHODS

Approaches used to overcome the challenges and successful delivery of PBL based courses during the pandemic included at-home labs with student acquired hardware, realistic simulations with integration of research and teaching, remote connections, and manipulation of hardware. The paper discusses application of these approaches in three representative courses and draw conclusions of their suitability in mostly online PBL.

ACTUAL OR ANTICIPATED OUTCOMES

The paper will provide a qualitative comparison of several teaching and learning approaches used to overcome specific challenges in PBL during the mostly online teaching in the pandemic. The paper will provide specific examples and details of the approaches listed above and provide a qualitative comparison.

CONCLUSIONS/RECOMMENDATIONS/SUMMARY

Through experience sharing of successful implementations of PBL during the pandemic, the paper will provide insights to develop tailored approaches for mostly online delivery of Electrical and Computer Engineering courses having strong PBL components.

KEYWORDS

Project based learning, blended learning, integration of research and teaching

Introduction

Project based learning (PBL) has been used across many Engineering disciplines to successfully deliver courses to achieve: better integration and blending of hands-on laboratory components with student learning; increased emphasis on applying the theoretical know how with practical problem solving; and integration of research related topics with undergraduate teaching (D. Andone and M. Frydenberg 2021, M. Huang and R. Yang 2021, M. Varas-Contreras et. al. 2021, R. C. Panicker and D. John 2021). Fundamental courses in early years of circuits and systems (CAS) curriculum involve standard lectures, guided labs and tutorial and problem-solving sessions to establish the core concepts, while CAS courses in the later years of the curriculum can strongly benefit from PBL. However, organisation and successful delivery of such PBL courses in CAS have been greatly challenged by numerous constraints imposed by the COVID-19 pandemic, mainly due to mostly online teaching with very limited access to laboratory resources, lack of opportunities for team-based projects and peer learning, limited interaction between students and teaching staff and poor student engagement.

In this paper, we share our experience and lessons learnt in delivering CAS courses having strong PBL components in two universities in Australia during the COVID-19 pandemic. To this end, we share the course details while outlining aims of using PBL in the courses, descriptions of PBL activities, methodologies used to successfully deliver them during the pandemic, specific challenges encountered, and approaches taken to overcome them.

Advanced Embedded System Design Course with PBL

The Advanced Embedded Systems course at university of Queensland is primarily a PBL course that is used for teaching Embedded RTOS (Real Time Operating System), embedded systems hardware and software frameworks in the 4th year of Engineering degree. Principles and practice of using wireless communications protocols (e.g. Bluetooth Low Energy), sensor fusion (e.g. Kalman filtering) and machine learning are also introduced. The lectures are given by guest lecturers from industry, who lecture on relevant topics such as RTOS, wireless communications, machine learning and sensor fusion, thus providing increased industrial exposure within the course. During the pandemic, lectures were conducted on Zoom.

At-Home Labs for Hands-on Work

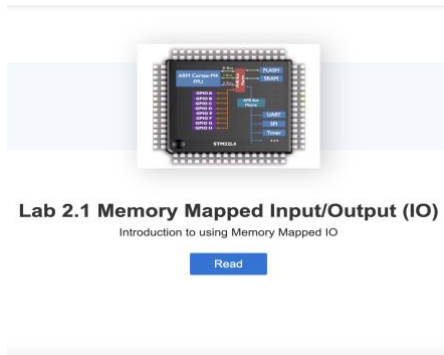
The course was offered in two modes, catering for both in-person and online students. For in-person students a hardware kit containing wireless development board and sensors was loaned. Online students were required to purchase the kit components from approved vendors and example online links were provided. During 2021 and 2022, all online students were able acquire their own hardware items and conduct hands-on lab work at home.

The course was structured around four lab assignments, a final project, and a final exam. The lab assignments had a strong PBL component and was managed through Zoom consultation sessions by tutoring staff with students completing the project activities at home or on campus. The lab assignments consisted of hardware design, wiring and firmware driver code writing. Typical project-based lab activities included programming with RTOS, working with web dashboards, implementing high-level algorithms and protocols to solve a particular problem such localisation, thus equipping students with components required for the later project. The lab assignments were assessed via online demonstrations. Code and documentation repositories were also used to assess the work offline.

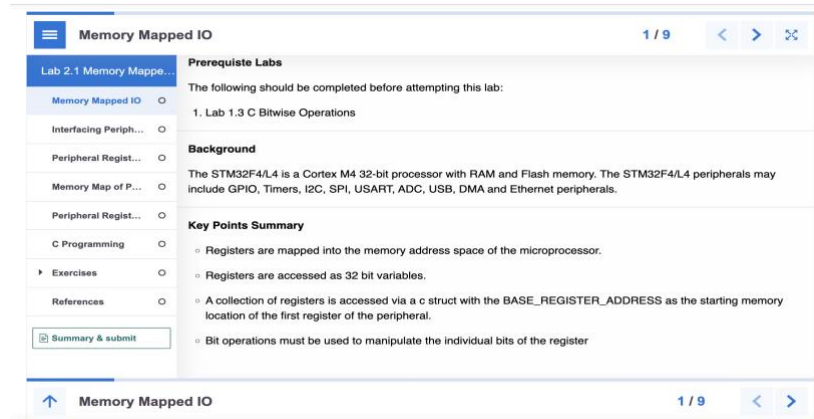
Online Tools for Labs – Blended learning via H5P

It was required to provide additional blended learning resources for at-home project-based labs. In addition to online lectures and other reading material, a rich set of asynchronous learning modules developed through H5P tools were used (<https://h5p.org/>). H5P allowed easy development and deployment of interactive web content including interactive workbooks and formative assessments.

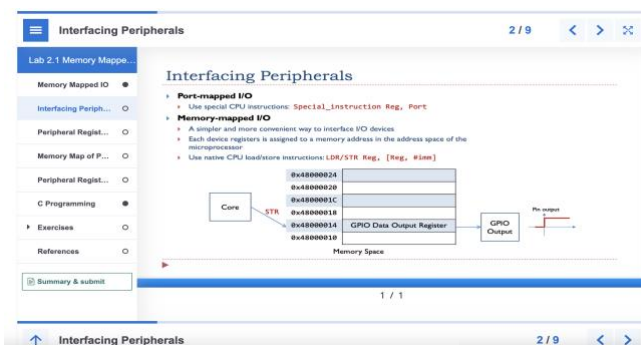
As shown in figure 1, such blended learning resources for a lab consists of introductory and in-depth content sections, and a quiz (not assessed). The quiz was used to provide feedback to the student on concepts covered in the lab module. The use of H5P based interactive and asynchronous resources had a positive impact on student engagement during mostly online project-based lab activities and was positively received by students.



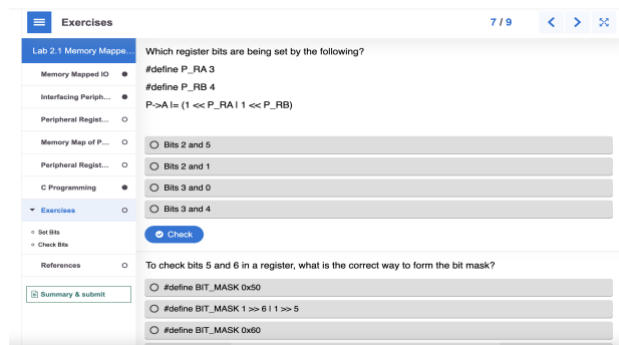
Lab module start



Introductory content



In-depth content



Quiz

Figure 1: Lab Module implemented with asynchronous and interactive H5P Workbook.

The final project was organised as a group project, with four students assigned per group. Students could choose which groups to join. Each group would choose from a list of topics that covered environmental sensing to indoor sensing. Each group was assessed according to the following criteria, shown in figure 2. The criteria ensures that key principles introduced in the lectures and labs are followed, such as sensor fusion, RTOS and wireless networking. The students were assessed in a final demonstration with a poster presentation. Each group was encouraged to deploy their solution, before the deadline, so that they could show continuous operation for a period of time.

Online Code Repository and Wiki

Each group used an online code repository and wiki, such as Github to organise and store their project implementation code and to document progress. The wiki was used to document the project, which included a progress (e.g. photos) and various implementation details. The use of repository and wiki features such as issues and milestones, allowed all group members and teaching staff to access and monitor the group's progress during mostly online teaching and to communicate or record any problems or challenges that occurred.

Requirements
Use physical sensors and/or actuators <ul style="list-style-type: none"> • At least one sensor other than temperature or voltage • If only one sensor is used, at least 2 parameters must be measured or calculated (e.g. RSSI and QoS)
Use non-trivial networking or communication with either <ul style="list-style-type: none"> • Mote-to-PC, PC-to-mote • Web/Cloud services
Used the following techniques/methods from lectures <ul style="list-style-type: none"> • Localization • Mobility, tracking • Time Synchronization • Sensing, Signal Processing • Other (approved by coordinator)
Key Performance Indicator (KPI) (Discussed or shown) <ul style="list-style-type: none"> • Completeness of project with achieving KPI outlined in project proposal. • Issues\challenges encountered and solutions outlined.
Project Configurability and Field Deploying Plan (Discussed or shown) <ul style="list-style-type: none"> • Outline of steps taken to ensure that the project firmware and hardware can be (re) configured for additional features. • Outline of steps taken to ensure that the project can be deployed in the field by non-project members.
Poster (template – printed or displayed on screen) <ul style="list-style-type: none"> • Aims (KPI) • System Overview • Results • Conclusions (Success of achieving KPI) • Presentation Format (concise dot points, clear diagrams)
Others <ul style="list-style-type: none"> • Innovation, Creativity • Props

Figure 2: Evaluation criteria for PBL group work.

Example Project 1: Methane Chamber Capture Group Project

An example project was the Methane Capture Chamber project, as seen in Figure 3. This project involved designing the hardware and software of a methane capture chamber, to measure the methane gas output of a lake water body. A floating platform had to be designed, to operate autonomously and on solar power. The methane sensor information had to be transmitted via a Wifi connection to a web dashboard. This project has been successfully running for 6 months, with the student group awarded a prize by the school for innovation.

Example Project 2: UWB Localisation Group Project

Another example project was the Ultra-Wide-Band (UWB) Localisation project, as seen in Figure 4. This project involved deploying a network of 12 UWB nodes for high accuracy localisation and tracking with in a confined space of a 5m by 10m. A localisation algorithm using Time of Arrival and Kalman filtering was developed. The group was able to successfully track a user, indoors, within 0.8m.

Greenhouse gas monitoring using a Capture Chamber

KPI'S

1. The device can measure methane with a sufficient accuracy
2. Able to detect unsafe methane levels and vent the chamber
3. Device can connect to a WiFi network and send protocol buffer encoded sensor readings to the Dashboard using MQTT.
4. Capable of running for a minimum of one day using the onboard battery and solar panel
5. The device can enter low power mode while not taking measurements

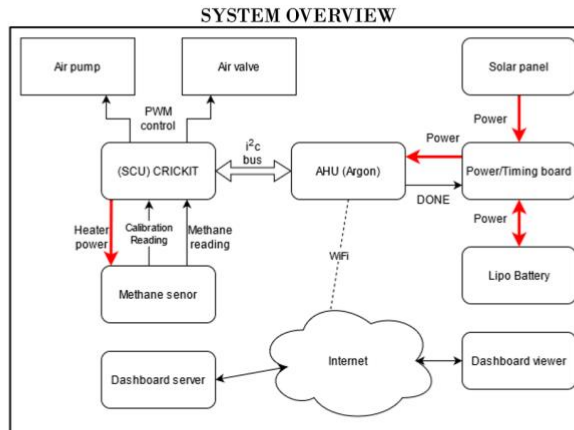
CHALLENGES

Problem: WiFi would sometimes fail to connect if the signal was poor.
Solution: Attempt connection multiple times.

Problem: No Zephyr library for interfacing with the CRICKIT board.

Solution: Port Adafruit C++ seesaw library to Zephyr (uses same i2c messaging protocol) and wrote drivers for the CRICKIT.

Problem: Excessive power usage running board and sensor continuously.
Solution: Periodically power on the system using a low power timer IC.



Capture chamber deployed during the day

CONCLUSIONS

- Device measured methane concentration to a good level of accuracy and consistency.
- Successfully vented the chamber at 750ppm. Concentration did not return to zero as the pump is too small, but it is sufficient to avoid dangerous methane concentrations
- Data was successfully sent to an online dashboard using protocol buffer encoding and MQTT over a WiFi connection.
- Measurement interval was set to 10 minutes for testing. The battery lasted for ~12 hours over night starting with 75% charge. For deployment measurements will be taken once per hour which will result in > 24 hours up-time.
- The device powered on at the chosen intervals, then powered off after measurements were taken and sent over WiFi.

RESULTS

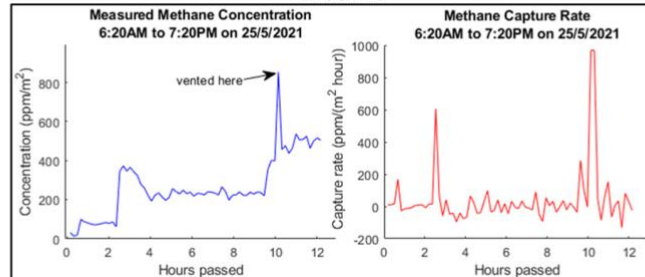


Figure 3: Example project that created a Methane Gas Emissions Monitoring System for a Lake.

ULTRA WIDE-BAND (UWB) AND BLUETOOTH AUGMENTED INDOOR LOCALIZATION NETWORK

Overview

This project involves developing a network of UWB/Bluetooth (DWM1001C) transceivers to track multiple people, across a few rooms in GP South. The aim is to minimize the number of nodes required to provide sufficient tracking of a moving target (tag).

This will be achieved by using a BLE mesh network containing advertising nodes, relays and a base station. The base station will receive the "flood" of information from the mesh network, process it and display it to an online dashboard (2D - map).

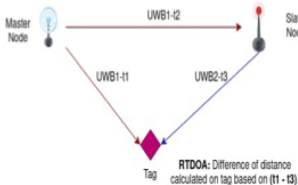
Key Performance Indicators

- Localisation accuracy: Use UWB to localise tags to a fine grain ranging accuracy of at least 0.8m.
- Minimizing static nodes: Use 6 nodes to accurately localize a tag.
- Persistent BLE Mesh provisioning: Once provisioned, the nodes remembers their association to the BLE mesh network.
- Scalability: The node network should be able to track multiple tags throughout the test area.
- Web-Dashboard: Location of tags able to be seen on web dashboard in close to real time.

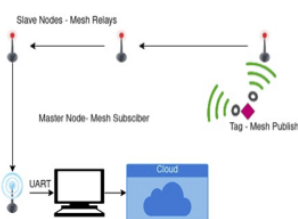
System Overview (Core Technologies)

- Localisation using reverse time difference of arrival (RTDOA).
- Node synchronization using time division multiple access (TDMA) and clock calibration packet service (CCP).
- Ranging calculation used to locate tags
- Data relay using BLE mesh network.

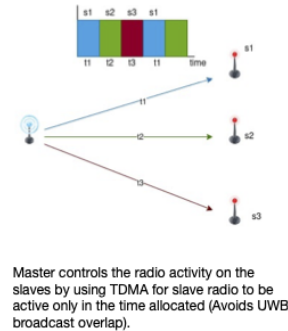
RTDOA Implementation



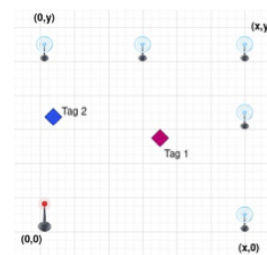
BLE Mesh Implementation



Node Synchronization Implementation



Operations Overview



Conclusion

Results Achieved

- Localisation accuracy:**
 - 0.8-1m of ranging accuracy
- Static nodes required:**
 - Ranging can be done with a minimum of 3 nodes.
- Persistent BLE Mesh:**
 - Mesh only requires to be provisioned once.
- PC Interface:**
 - Dynamically adaptable to custom grid sizes.
- Tags Tracked:**
 - No theoretical max for the number of tags that can be tracked (RTDOA).
- Web Dashboard:**
 - Tag location updated on dashboard approximately every 600ms.

Limitations

- BLE Mesh Bottleneck:**
 - The mesh has a cap on the amount of data that can be transferred without implementing segmented data messages. This limits the numbers of tags that can be active at once.
- Scalability:**
 - The maximum grid size is limited by the masters UWB range (Required for TDMA).

Improvements

- Time synchronisation with BLE Mesh
- Segmented data messages BLE Mesh (Increase packet transfer)
- Implement PC interface to display tag location (in real time)

Figure 4: Example Project that created an Indoor Person Tracker using Ultrawide Band Localisation.

Digital System Design Course

The digital system design course at university of Queensland was a 4th year Engineering core course focusing on hardware description languages and digital design on field programmable gate array (FPGA) based platforms. While this course employed standard lectures and design-oriented lab activities, an attempt was made to integrate research with undergraduate education via PBL, which has been successfully received by students. The course was offered for both in-person and online cohorts of students, mainly relying on Zoom lectures and Zoom based lab classes with remotely access lab computers via remote desktop connections.

During the 13-week semester students completed 5 lab tasks which are semi-guided. Labs were conducted in both in-person (with some interruptions due to campus closure) and online and were mostly simulation based. Students used Xilinx Vivado design environment to design and simulate various combinational and sequential digital blocks followed by finite state machines and controller-data path designs. During the mostly online teaching, students accessed lab computers via remote desktop connections and also used their own computers to run the functional and post-synthesis simulations. For each lab task, students submitted a detailed lab report, and a separate oral assessment was also conducted either on Zoom or in-person. Identity verified oral assessments were used as a remedy for increased academic integrity issues during the pandemic. Teaching staff conducted the oral assessments in the form quick technical interviews, thus providing students an opportunity to improve their verbal communication skills in addition to report writing.

Following the semi-guided lab activities which were mainly based on RTL descriptions using HDL based approach a design project was introduced during weeks 10-13 of the semester with the following objectives: introduce some of the high-level synthesis and model-based system design approaches used in FPGA based system design; integrate research with undergraduate teaching via a tailored project having a research flavour; suitable to be completed and assessed via mostly online simulation based approach, yet challenging and engaging. The project involved design and simulation of a hardware accelerator for a wideband digital beamforming algorithm using Xilinx Model Composer tool flow which integrates Matlab/Simulink and Xilinx Vivado tools to allow rapid prototyping of digital signal processing (DSP) algorithms. This involved the design of parallel hardware accelerated architectures for two-dimensional (2D) finite impulse response (FIR) and infinite impulse response (IIR) filters and optimising the design for FPGA resources and timing constraints. Such topics are typically encountered in master's level research and often require reading published literature. Thus, the effort was to integrate such topics with undergraduate teaching via PBL using suitable projects for simulation based online mode of delivery.

With sufficient guidance provided by teaching staff, students were required to do their own research first to transform their knowledge of one-dimensional filter implementation on an FPGA to obtain parallel hardware architectures suitable for 2D FIR and 2D IIR filters. Teaching staff provided broader context to the project using realistic Matlab simulations and interactive tutorial discussions during online lab classes. Subsequently, a set of design steps are provided to guide students through an iterative design process where an initial design is iteratively optimised to meet certain timing and area constraints. Students had to adapt some of the pipelining and parallel processing techniques they learnt for 1D filters to 2D filters and also optimise for the effects of fixed-point finite precision word lengths within their designs. Finally, students were assessed based on a final report and their design files submitted via the learning management system.

The use of research related project suitable for simulation-based approach in a PBL context was well received by students as evident by some of the student feedback quoted below.

“Interesting and enjoyable practical work. Project was practical, relevant, and had real-life applications”

“I really liked the subject material, and the lectures were interesting and well structured. Even though the pracs were all conducted in the form of simulations, they were still engaging, informative, and a reasonable level of difficulty”

Engineering Design Course with PBL

This course is a 4th-year core course at University of New South Wales, which is aimed at ensuring electrical engineering graduates have accomplished a minimum level of individual proficiency in core disciplines of Electronics, Signal Processing and Control Systems, and group-work skills in a fourth elective topic. All formal lab sessions are assessment opportunities. Students are challenged through a sequence of design tasks in each core topic, while in the fourth elective topic, they will form groups of up to 4 to work on a more comprehensive design task to expose them to a healthy balance between teamwork and individual responsibility. During the COVID-19 pandemic, everything had to be shifted to cater for online delivery while trying to preserve the crucial aspects of the learning outcomes. The main objective in this transition was to ensure that the new project tasks would encompass most of the practical aspects of a real-world project that they would have otherwise been able to encounter and work with in a face-to-face mode. It was also important to maintain the integrity of the assessments around the project and its alignment with the course learning outcomes.

The tasks were converted into simulation-based design challenges. For instance, the Electronics design tasks were required to be completed in the LTspice software with an attached component for preparing a Printed Circuit Board (PCB) for each of the final designs. The tasks themselves were derived from different components of a biomedical device developed by a start-up company. Enforcing the PCB design for each LTspice simulation would ensure that students can gain the practical skills on realising a circuit design into a final prototype ready for manufacturing.

For the Control Systems topic as another example, a realistic virtual model of an actual device was created in MATLAB/Simulink software. For these set of design challenges, a detailed physical model of a DC motor-generator setup (Figure 5(a)) was derived in the lab using empirical data, including estimation of nonlinear friction. To ensure that students can treat this model like an actual device in the virtual mode, the identified parameters were randomised for each student around an acceptable tolerance. These parameters were then used to create a virtual 'black box' model for each student by embedding them in a 'protected model' in Simulink as shown in Figure 5(b) and 5(c). This means each student would have their own individual system without knowing the physical parameters. The students were then able to conduct the virtual experiments to perform system identification as part of the process modelling and designing a control system for their own model based on the project description in that virtual environment.

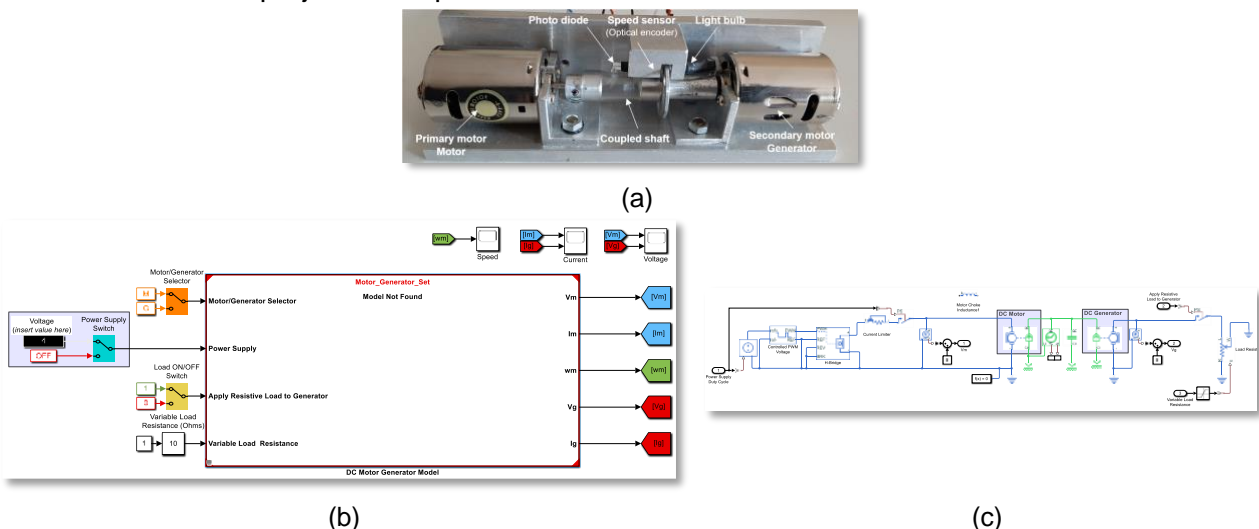


Figure 5: (a) The DC motor-generator used to create the virtual model, (b) The protected Simulink model of the device with embedded randomised parameter, (c) Detailed physical implementation of the actual device inside the protected model.

The course was delivered fully online through Microsoft (MS) Teams as the learning management system (LMS). There were two 3-hour lab sessions every week, with 1 lab demonstrator per 8 students for a class of 120 students. An on-demand queuing dashboard system was developed

using MS Forms and Power Automate within MS Teams to provide a similar environment where students can engage and communicate with course staff and lab demonstrators. This system allowed students make a request via MS Form in the course channel for general query about the design tasks or ask to get assessed during the scheduled lab time. The queue was updated automatically to show the status of each student request and the lab demonstrator who attended to each request the entire class in the course channel (student view). This system enabled a dedicated MS Teams meeting to be created and sent to the student through Power Automated chat-bot who made the request once an available lab demonstrator accepted the request in the private course staff channel (lab demonstrator view). The student and the lab demonstrator would then join the meeting to either go through the student query or run an assessment. Lab demonstrators, who were trained on online assessment for each design task, would go through checking each requirement of the design task with the student as they share their screen and submit all the files in advance. A major component of the live interview during the meeting was to challenge students with some understanding questions around the design task in addition to checking their results. This was done to ensure that students could demonstrate full understanding of their deliverables and results. The process for this dashboard system is shown in Figure 6.

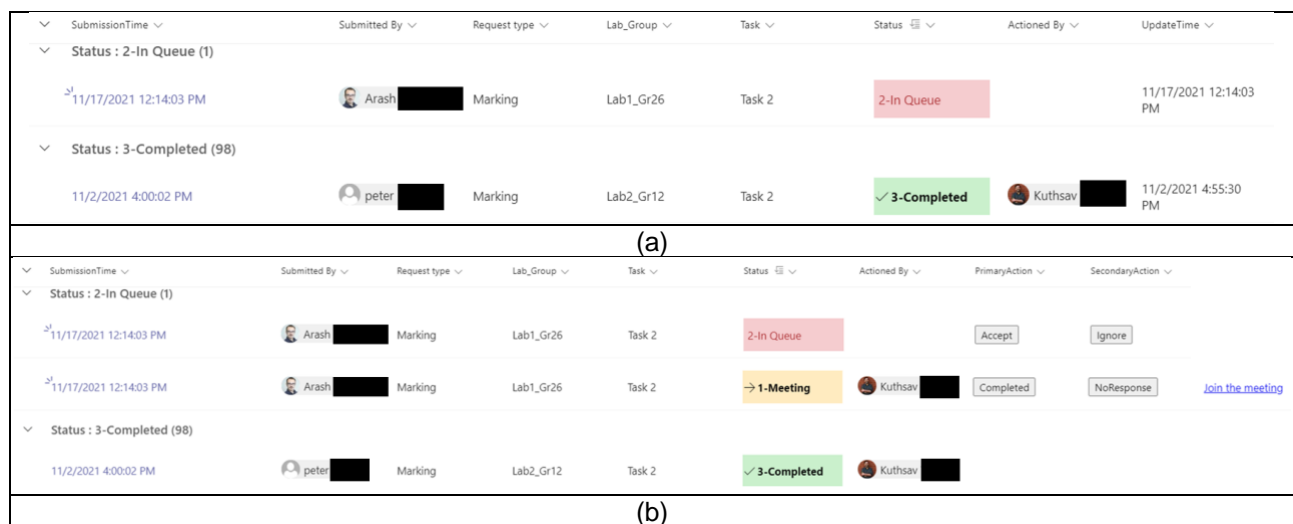


Figure 6: The online dashboard system, (a) Student view, (b) Lab demonstrator view.

Each core design topic would run for two weeks in a 10-week trimester (total 6 weeks). The group-work section of the course ran in a similar fashion in the last 3 weeks (with Week 6 as term break). A lab demonstrator would be assigned as a mentor for each group to support them during the schedule lab times. Each group would prepare a short presentation every week as a progress review to be assessed during a live MS Teams meeting by their mentor. The final assessment of the teamwork design task was conducted at the end of the term.

Every stage of this transition into virtual mode was communicated to students with clear expectations. Extra support and feedback were offered on their performance along the way during lockdown through Student Forum and weekly consultation meetings which were recorded for those who would not be able to attend.

Analysis, Discussion and Conclusions

To better observe the impact of transitioning of PBL based courses to online, we present an analysis for the Engineering Design Course at University of New South Wales. We compare the performance of students in the online offering of this course in Term 3 2021 with the previous face-to-face (F2F) offering of this course in Term 3 2019 before the pandemic with similar number of students. Fig. 7 shows the final mark distribution of the course. As can be seen, at the low and high mark range, the performance seems to be more or less consistent between online and F2F students. However, in online mode, students were either performed really well (43 student with a mark greater than or equal to 80) or struggled at the lower end (58 students with a mark lower than or equal to 69) with very few sitting at the 70-79 mark range, unlike the majority of F2F students who are in that range. This indicates that even with all the online support resources, average students might either fall through the cracks and miss out on engaging with the course or they would thrive and perform quite well, as opposed to F2F students where the majority achieved an expected average mark in the course.

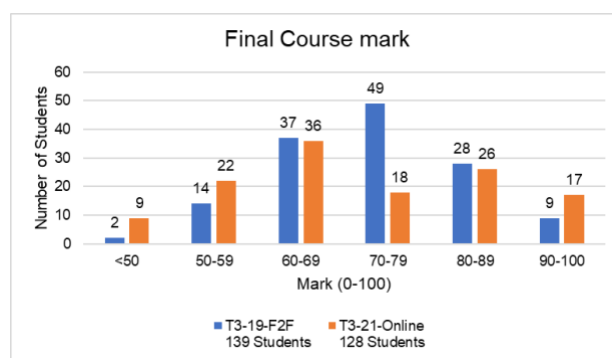


Figure 7: Comparison of mark distribution between F2F and online delivery of the design course engineering design course with PBL.

In conclusion, while the complete online delivery of strong PBL courses is still remains a challenge, we have managed to employ a variety of blended learning tools and approaches to maintain the hands-on aspects of the example courses presented, while delivering in mostly online form. Future work includes a detailed study on the impact of student learning and engagement.

References

- D. Andone and M. Frydenberg (2021), *Adapting Project-Based Learning through Virtual Mobilities in Pandemic Times - TalkTech Project Revisited*, International Conference on Advanced Learning Technologies (ICALT), pp. 1-3.
- M. Huang and R. Yang (2021), *Action Research on Project-based Learning and Contest-based Learning in Higher Education*, International e-Engineering Education Services Conference (e-Engineering), 2021, pp. 17-22
- M. Varas-Contreras, J. Maluenda-Albornoz, D. Chacano-Osses and J. Vidal-Valenzuela (2021), *Effects of a Project Based Learning on the development of innovation competencies in online mode in first-year engineering students*, 40th International Conference of the Chilean Computer Science Society (SCCC), 2021, pp. 1-5
- R. C. Panicker and D. John (2021), *Fully Remote Project-Based Learning of Hardware/Software Codesign*, 2021 IEEE Frontiers in Education Conference (FIE), 2021, pp. 1-5

Copyright statement

Copyright © 2022 Matthew D'Souza, Arash Khatamianfar, and Chamith Wijenayake. The authors assign to the Australasian Association for Engineering Education (AAEE) and educational non-profit institutions a non-exclusive licence to use this document for personal use and in courses of instruction provided that the article is used in full and this copyright statement is reproduced. The authors also grant a non-exclusive licence to AAEE to publish this document in full on the World Wide Web (prime sites and mirrors), on Memory Sticks, and in printed form within the AAEE 2022 proceedings. Any other usage is prohibited without the express permission of the authors